

G120 SoM Pinout

Many signals on the G120 are multiplexed to offer multiple functions on a single pin. Developers can decide on the pin functionality to be used through the provided libraries. Any pin with no name, function, or note must be left unconnected.

Pin	Name	Function	Pin	Name	Function	Pin	Name	Function
1		GND	32	P4.29	COM4 RX	63 ¹	P0.27	I2C SDA
2		3.3 V	33	P1.14		64	P3.25	PWM7
3	P2.4	PWM10 LCD OE	34	P1.17		65	P3.24	PWM6
4	P2.8	LCD R3	35	P1.16		66		USBC D+
5	P0.0	CAN1 RD	36	P1.15		67		USBC D-
6	P0.10	COM3 TX	37	P1.9		68		USBH D-
7	P2.11		38	P1.10		69		USBH D+
8	P2.10	LDR0	39	P1.4	SPI3 MISO	70	P2.21	
9	P0.11	COM3 RX	40	P1.8		71	P1.22	LCD G2
10	P0.1	CAN1 TD	41	P1.1	SPI3 MOSI TOUCH XR	72	P1.21	LCD G1
11	P0.18	SPI1 MOSI	42	P1.0	SPI3 SCK TOUCH YD	73	P1.19	COM3 OE
12	P0.16	COM2 RX	43			74	P1.23	LCD G3
13	P0.15	SPI1 SCK	44			75	P1.24	LCD G4
14	P0.22	LDR1	45		GND	76	P1.20	LCD G0
15	P0.17	COM2 CTS SPI1 MISO	46		3.3 V	77	P1.25	LCD G5
16	P2.1	MODE	47	P0.3	COM1 RX	78	P1.26	LCD B1
17	P0.6	COM2 RTS COM2 OE	48	P0.2	COM1 TX	79	P1.28	LCD B3
18	P2.0	COM2 TX	49	P0.26	ADC3 DAC0	80	P1.29	LCD B4 COM5 TX
19		SPI2 SCK	50	P0.24	ADC1 TOUCH YU	81		GND
20		SPI2 MISO	51	P0.25	ADC2	82	P1.27	LCD B2
21		SPI2 MOSI	52	P0.23	ADC0 TOUCH XL	83	P2.13	LCD B0
22	P1.12	SD D3	53		RESET	84	P2.12	LCD R0
23	P1.11	PWM5 SD D2	54	P3.26	PWM8	85	P2.5	LCD HS PWM11
24	P1.7	PWM4 SD D1	55	P0.13	ADC7	86	P2.2	LCD CLK
25	P1.2	PWM0 SD CLK	56	P0.12	ADC6	87	P2.7	LCD R2
26	P1.6	PWM3 SD D0	57		RTC CRYSTAL 1	88	P2.9	LCD R4 COM5 RX
27		GND	58		RTC CRYSTAL 2	89	P2.6	LCD R1
28	P1.3	PWM1 SD CMD	59 ²	P1.30	ADC4 COM4 OE	90	P2.3	LCD VS PWM9
29	P0.5	CAN2 TD	60		VBAT	91	P1.5	PWM2
30	P0.4	CAN2 RD	61 ¹	P0.28	I2C SCL			
31	P4.28	COM4 TX	62	P1.31	ADC5			

¹Open drain requiring a 2.2 kΩ pull-up resistor

²Must not be low on startup