

G30 SoC Pinout

Many signals on the G30 are multiplexed to offer multiple functions on a single pin. Developers can decide on the pin functionality to be used through the provided libraries. Any pin with no name, function, or note must be left unconnected.

Pin	Name	Function	Pin	Name	Function	Pin	Name	Function
1		VBAT	23	PA7	ADC7	45	PA12	USBC D+
2 ⁵	PC13	LDR1	24	PC4	ADC14	46	PA13	
3 ⁵	PC14	RTC XTAL IN	25	PC5	ADC15	47		GND
4 ⁵	PC15	RTC XTAL OUT	26	PB0	ADC8	48		3.3 V
5		SYS XTAL IN	27	PB1	ADC9	49	PA14	
6		SYS XTAL OUT	28 ¹	PB2		50	PA15	LDR0
7		RESET	29	PB10	MODE	51	PC10	SD D2
8	PC0	ADC10	30 ²			52 ³	PC11	SD D3
9	PC1	ADC11	31		GND	53	PC12	SD CLK
10	PC2	ADC12	32		3.3 V	54	PD2	SD CMD
11	PC3	ADC13	33	PB12		55	PB3	SPI1 SCK
12 ⁶		GND	34	PB13	SPI2 SCK	56	PB4	SPI1 MISO
13 ⁶		3.3 V	35	PB14	SPI2 MISO	57 ³	PB5	SPI1 MOSI
14	PA0	ADC0	36	PB15	SPI2 MOSI	58 ⁴	PB6	I2C SCL
15	PA1	ADC1	37	PC6	PWM7	59 ⁴	PB7	I2C SDA
16	PA2	ADC2	38	PC7	PWM8	60 ¹		
17	PA3	ADC3	39	PC8	PWM9	61	PB8	PWM13
18		GND	40	PC9	PWM10	62	PB9	PWM14
19		3.3 V	41	PA8	PWM0	63		GND
20	PA4	ADC4	42	PA9	COM1 TX	64		3.3 V
21	PA5	ADC5	43 ³	PA10	COM1 RX			
22	PA6	ADC6	44	PA11	USBC D-			

¹Requires a 10 kΩ pull-down resistor

²Requires a 4.7 μF capacitor to GND

³Requires a 10 kΩ pull-up resistor

⁴Open drain requiring a 2.2 kΩ pull-up resistor

⁵Can sink 3 mA, cannot source

⁶Used for the analog system