

G80 SoC Pinout

Many signals on the G80 are multiplexed to offer multiple functions on a single pin. Developers can decide on the pin functionality to be used through the provided libraries. Any pin with no name, function, or note must be left unconnected.

Pin	Name	Function	Pin	Name	Function	Pin	Name	Function
1	PE2		35	PB0	ADC8 PWM10	69 ²	PA10	COM1 RX
2	PE3	LDR0	36	PB1	ADC9 PWM11	70	PA11	USBC D-
3	PE4	LDR1	37 ¹	PB2		71	PA12	USBC D+
4	PE5		38	PE7		72	PA13	
5	PE6		39	PE8		73 ³		
6		VBAT	40	PE9	PWM0	74		GND
7 ⁵	PC13		41	PE10		75		3.3 V
8 ⁵	PC14	RTC XTAL IN	42	PE11	PWM1	76	PA14	
9 ⁵	PC15	RTC XTAL OUT	43	PE12		77	PA15	PWM4
10		GND	44	PE13	PWM2	78	PC10	SD D2
11		3.3 V	45	PE14	PWM3	79 ²	PC11	SD D3
12		SYS XTAL IN	46	PE15	MODE	80	PC12	SD CLK
13		SYS XTAL OUT	47	PB10	PWM6 SPI2 SCK	81	PD0	CAN1 RD
14		RESET	48 ²	PB11	PWM7	82	PD1	CAN1 TD
15	PC0	ADC10	49 ³			83	PD2	SD CMD
16	PC1	ADC11	50		3.3 V	84	PD3	COM2 CTS
17	PC2	ADC12 SPI2 MISO	51	PB12	CAN2 RD	85	PD4	COM2 RTS
18	PC3	ADC13 SPI2 MOSI	52	PB13	CAN2 TD	86	PD5	COM2 TX
19		3.3 V	53	PB14	USBH D-	87	PD6	COM2 RX
20 ⁶		GND	54	PB15	USBH D+	88	PD7	
21		3.3 V	55	PD8	COM3 TX	89	PB3	PWM5 SPI1 SCK
22 ⁶		3.3 V	56	PD9	COM3 RX	90	PB4	PWM8 SPI1 MISO
23	PA0	ADC0 COM4 TX	57	PD10		91 ²	PB5	PWM9 SPI1 MOSI
24	PA1	ADC1 COM4 RX	58	PD11	COM3 CTS	92 ⁴	PB6	I2C SCL
25	PA2	ADC2 PWM20	59	PD12	COM3 RTS PWM12	93 ⁴	PB7	I2C SDA
26	PA3	ADC3 PWM21	60	PD13	PWM13	94 ¹		
27		GND	61	PD14	PWM14	95	PB8	PWM22
28		3.3 V	62	PD15	PWM15	96	PB9	PWM23
29	PA4	ADC4 DAC1	63	PC6	PWM16	97	PE0	
30	PA5	ADC5 DAC2	64	PC7	PWM17	98	PE1	
31	PA6	ADC6 PWM24	65	PC8	PWM18 SD D0	99		GND
32	PA7	ADC7 PWM25	66	PC9	PWM19 SD D1	100		3.3 V
33	PC4	ADC14	67	PA8				
34	PC5	ADC15	68	PA9	COM1 TX			

¹Requires a 10 kΩ pull-down resistor

²Requires a 10 kΩ pull-up resistor

³Requires a 2.2 μF capacitor to GND

⁴Open drain requiring a 2.2 kΩ pull-up resistor

⁵Can only sink up to 3 mA, cannot source

⁶Used for the analog system